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		Application Number		10/759,666	
		Filing Date			
		First Named Inventor		Junaid Ahmed Siddiqui	
		Art Unit			
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	06406 USA

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
~		R.K. ILER, The Chemistry of Silica, Wiley Interscience, 1979, P. 410-411.	✓
~		B.L. MUELLER, et al, Polishing Surfaces for Integrated Circuits, Chemtech, Feb. 1998, P. 38-46.	✓
~		H. LANDIS, et al, Thin Solids Films, 220, P. 1. "Integration of chemical mechanical polishing into CMOS integrated circuit manufacturing"	✓
		Date unknown	

Examiner Signature		Date Considered	7/25/05
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